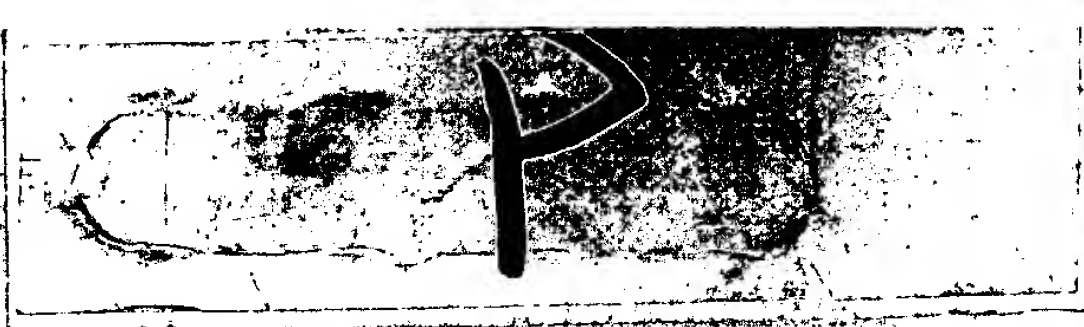


Class	Subclass	ISSUE CLASSIFICATION



PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>AK</i> Q.A. <i>WJ</i>	PATENT DATE
--	-------------

APPLICATION NO. 09/956909	CONT/PRIOR D F	CLASS 361	SUBCLASS 763	ART UNIT 2841 2827	EXAMINER Dinh
------------------------------	-------------------	--------------	-----------------	--------------------------	------------------

APPLICANTS
 Yutaka Akiba
 Yasunori Narizuka
 Hirayoshi Tanei
 Naoya Kitamura

TITLE
 Low-EMI electronic apparatus, low-EMI circuit board, and method of manufacturing the low-EMI circuit board

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION									

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.			1		
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
	_____ (Legal Instruments Examiner) _____ (Date)			Amount Due	Date Paid
				ISSUE BATCH NUMBER	

WARNING:
 The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A
(Rev. 8/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
 (Attached in pocket on right inside flap)